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~~Apple ARM - RISC vs CISC (Bye Intel)CISC DESIGN ARCHITECTURE OF CPU 6th - 7th - Pre-9th - New 9th -[English] - Lecture3 - Waqar Haider - CISC Online Lecture Series Andrew Grove: Only the Paranoid Survive Book Summary Why Apple ARM Implementation is Faster (David Patterson) | AI Podcast Clips with Lex Fridman~~

~~RISC vs CISC - Is it Still a Thing?RISC vs CISC Uterine Fibroids Homeopathic Treatment - Uterus Lump, Uterus Tumor, Rasoli, Symptoms and Causes RISC VS CISC - CPU architecture Information System, Its impact on Organization and Society Uterine Leiomyoma (Fibroids) - CRASH! Medical Review Series RISC \u0026 CISC - Example described What's the Difference Between RISC and CISC? Management Information Systems Chapter 2 CISC 181 MIS Chapter 4 Hardware and Software Ice Fishing Lake Trout \u0026 Cisc 6th - 7th - Pre-9th - New 9th - [English] - Lecture#23 - Waqar Haider - CISC Online Lecture Series~~

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Complex instruction set computer - Wikipedia

CISC design is a 32 bit processor and four 64-bit floating point registers. VAX 11/780 – CISC design is a 32-bit processor and it supports many numbers of addressing modes and machine instructions which is from Digital Equipment Corporation.

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