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**Multiple Choice Answers For Digital**

Answer : 1. Q40. The output of a half adder is ..... Sum; Sum and Carry; Carry; none of these; Answer: 2. Q41. Minimum number of 2-input NAND gates required to implement the function  $F = (x + y)(Z + W)$  is ..... 3; 4; 5; 6; Answer : 2. Explanation: Q42. Which device has one input and many outputs? Multiplexer; Demultiplexer; Counter; Flip flop; Answer: 2

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c. Transmission of digital data d. All of the above. ANSWER: (a) Removing long strings of 1's and 0's. 33) In polar RZ format for coding, symbol '0' is represented by. a. Zero voltage b. Negative voltage c. Pulse is transmitted for half the duration d. Both b and c are correct. ANSWER: (d) Both b and c are correct. 34) In a uni-polar RZ format, a.

## Multiple Choice Questions and Answers on Digital Communication

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Digital Communication MCQ Questions and Answers Quiz. 1. Two binary values are represented by two different frequencies in : frequency-shift-keying. phase-shift-keying. amplitude-shift-keying. none of these above. Answer-1.

## Digital Communication Multiple Choice Questions and Answers

MCQ quiz on digital electronics multiple choice questions and answers on digital electronics objective question and answer to prepare for technical entrance test and competitive exams. Page 2

## Digital Electronics Multiple Choice Questions and Answers ...

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## Digital Logic Design Multiple Choice Questions Answers ...

Multiple choice Questions on Digital Transmission. Practice for BBA or MBA exams using these MCQ. Page 3. ... analog-to-digital; View answer. Correct answer: (D) analog-to-digital. 30. The \_\_\_\_\_ scheme uses data patterns of size 2 and encodes the 2-bit patterns as one signal element belonging to a four-level signal.

## Digital Transmission Multiple choice Questions and Answers ...

1. \_\_\_\_\_ conversion involves three techniques: line coding, block coding, and scrambling. Analog-to-digital. Digital-to-analog. Analog-to-analog. Digital-to-digital. View answer. Correct answer: (D) Digital-to-digital. 2. \_\_\_\_\_ encoding has a transition at the beginning of each 0 bit.

## Digital Transmission Multiple choice Questions and Answers ...

Multiple choice Questions Digital Logic Design 1. How many bits must each word have in one-to-four line de-multiplexer to be implemented using a memory? A. 8 bit B. 4 bits C. 2 bits D. 1 bits 2.

## Multiple choice Questions Digital Logic Design ...

Multiple Choice Questions and Answers on Digital Electronics. 1. Each term in the standard POS form is called a. A) maxterm. B) minterm. C) literal. D) don't care. 2. The NOR-NOR realization is equivalent to.

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Digital Marketing Multiple Choice Questions:-. 1. Delivering different messages to members of a business decision-making unit is a key difference between B2C and B2B marketing which is reflected in web design through \_\_\_\_\_. E. None of the above. 2. A longer decision-making process than for many consumer products is a key difference between B2C and B2B marketing which is reflected in web design through \_\_\_\_\_.

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